



## General Description

The LN8230 provides a high integration solution for lithium-ion/polymer battery protection. The LN8230 contains advanced power MOSFET, high-accuracy voltage detection circuits and delay circuits.

The LN8230 has all the protection functions required in the battery application including over-charging, over-discharging, over-current and load short circuiting protection etc. The low standby current drains little current from the cell while in storage.

The device is targeted for any Li-Ion and Li-Poly battery-powered information appliances requiring long-term battery life.

#### Features

- Protection of Charger Reverse Connection
- Protection of Battery Cell Reverse Connection
- High-accuracy Voltage Detection
- Charger Detection Function
- 0V Battery Charging Enable/Disable Function
- Integrate Advanced Power MOSFET( $50m\Omega$ )
- Low Current Consumption
  Operation Mode: 3.0µA typ.
  Power-down Mode: 0.1µA max.

## Application

Package

• One-Cell Lithium-ion Battery Pack

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DFN2x2-6L

(TOP VIEW)

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1

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SOT-23-5L

(TOP VIEW)

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• Lithium-Polymer Battery Pack



## Application Information

## **Functional Pin Description**

#### SOT23-5L DFN2x2-6L Pin Note TM 1 Test terminal. Floating or Conncet to GND \_ GND 2 2, 3 Ground VIN 3 1 Power Supply BATN 4 4, 5, 6 The negative terminal of battery pack HEARTSINK 7 Heatsink must be connected to BATN \_

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# Ordering Information

## LN8230 12345 - G

Designator	Symbol	vov	VOVR	VUV	VUVR
	C5A	4.300V	4.150V	2.500V	2.900V
	C5B	4.280V	4.150V	2.500V	2.900V
123	C5C	4.430V	4.250V	2.500V	2.900V
	C5D	4.400V	4.250V	2.500V	2.900V
	М	SOT23-5L			
(4)	D	DFN2x2-6L			
(	R	Reel			
	Т	Таре			
G					

# Marking Information



Designator	Symbol	Description	
1	С	LN8230	
2	А	VOV: 4.300V, VOVR: 4.150V	
	В	VOV: 4.280V, VOVR: 4.150V	
	С	VOV: 4.430V, VOVR: 4.250V	
	D	VOV: 4.400V, VOVR: 4.250V	
3	М	SOT23-5L	
	D	DFN2x2-6L	
4	Tracking information for internal quality		



## Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Supply Voltage	VIN	GND-0.3~GND+6.0	V
Power Dissipation	PD@TA=25℃	0.4	W
Package Thermal Resistance(Junction to Ambient)		130	°C/W
Lead Temperature		260	
Ambient Temperature	Тора	-40~+85	C
Storage Temperature	Tstr	-55~+125	
ESD Susceptibility	HBM	4000	V

## ■ Function Block Diagram





## Note: $V_{VIN} = 3.5V$ , $T_A = 25$ °C, unless otherwise specification.

Parameters	Test Condition	Min	Тур	Max	Unit
Voltage		-			
Over-Charge Detection Voltage		4.27	4.30	4.33	V
Over-Charge Release Voltage		4.12	4.15	4.18	V
Over-Discharge Detection Voltage		2.46	2.50	2.54	V
Over-Discharge Release Voltage		2.86	2.90	2.94	V
Charger Detection Voltage			-0.50		V
Current					
Over-Discharge Current Detection 1	V <sub>DD</sub> =3.5V		1		А
Over-Discharge Current Detection 2	V <sub>DD</sub> =3.5V		5		А
Load Short Detection	V <sub>DD</sub> =3.5V		5		А
Normal Operator Current	V <sub>DD</sub> =3.5V, V <sub>BATN</sub> =0V		3.0	6	uA
Power Down Current	V <sub>DD</sub> =2.0V, BATN floating			0.1	uA
Delay Time	-	-	-		
Over-Charge Voltage Detection Delay Time			200		mS
Over-Discharge Voltage Detection Delay Time			50		mS
Over-Discharge Current Detection 1 Delay Time			12		mS
Over-Discharge Current Detection 2 Delay Time			2.5		mS
Load Short Detection Delay Time			40		uS
BATN Internal Resistance					
Internal Resistance between BATN and $\mathrm{V}_{\mathrm{DD}}$	V <sub>DD</sub> =3.5V BATN=1.0V		320		kΩ
Internal Resistance between BATN and GND	V <sub>DD</sub> =2.0V BATN=1.0V		100	-	kΩ
				-	
Power FET on Resistance	$V_{DD}$ =3.5V, $I_{BATN}$ =1.0A		50		mΩ
Over Temperature Protection			100		°C
Over Temperature Hybrid			20		°C
Note: Parameter with * is used for 4.35V Battery Protect, Other is for 4.2V Battery Protect					



### Typical Operating Characteristics

T he LN8230 monitors the voltage and current of a battery and protects it from being damaged due to overcharge voltage, over-discharge voltage, over-discharge current, and short circuit conditions by disconnecting the battery from the load or charger. These functions are required in order to operate the battery cell within specified limits.

#### Normal operating mode

If no exception condition is detected, charging and discharging can be carried out freely. This condition is called the normal operating mode.

#### **Over-Charge Condition**

When the battery voltage becomes higher than the over-charge detection voltage ( $V_{CU}$ ) during charging under normal condition and the state continues for the overcharge detection delay time ( $t_{CU}$ ) or longer, the LN8230 turns off the power switch FET.

#### **Over-Discharge Condition**

When the battery voltage drops below the over-discharge detection voltage ( $V_{DL}$ ) during discharging under normal condition and it continues for the over-discharge detection delay time ( $t_{DL}$ ) or longer, the LN8230 turns off the power switch FET and stops discharging. After over-discharging condition, the BATN pin is pulled up to high state by the internal control of LN8230, the current of the chip is reduced to the power-down current ( $I_{PDN}$ ).

At the over-discharge condition, when a charger is connected, the power switch FET is still off. At this time, charging is still permitted through the parasitic diode of the power switch FET, when the battery voltage becomes higher than the over-discharge detection voltage ( $V_{DL}$ ), the LN8230 turns on the power switch FET and changes to the normal condition from the over-discharge condition.

#### **Over-Current Condition**

When the discharging current becomes equal to or higher than a specified value during discharging under normal condition and the state continues for the over-current detection delay time or longer, the LN8230 turns off power switch FET to stop discharging, and the BATN pin is pulled up to high state by the internal control of LN8230. This condition includes over-current1, over-current2 or load short-circuiting.

The over-current condition returns to the normal mode when the load is released or the impedance between BATP and BATN is larger than  $500k\Omega$ .

The LN8230 provides two over-current detection levels (3A and 10A ) with two over-current delay time ( $T_{OI1}$  and  $T_{OI2}$ ) corresponding to each over-current detection level.









- Package Information
- SOT23-5L



Symbol	Dimensions In	Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950(E	BSC)	0.037(	BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



DFN2x2-6L



COMMON DIMENSION (MM)					
PKG	DFN2020-6L				
SYMBOL	MIN.	NDM.	MAX		
A	0.527	0.552	0.577		
b	0.20	0.25	0.30		
D	1.90	2.00	2.10		
E	1.90	2.00	2.10		
D1	0.80	0.90	1.00		
E1	1.60	1.70	1.80		
e	0.65 REF.				
L	0.25	0.30	0.35		